Acceleration technologies for high-performance computing
About DRC

Founded in 2004 – HQ in Sunnyvale, CA

Announced OEM relationship with Cray in May 2006.

Programmable coprocessors for compute-intensive applications

Complete development platform for developing accelerated applications
DRC RPU™ Coprocessor

- Plugs directly into an existing AMD processor socket
- Works with standard motherboards
- Has all the advantages the socket provides

Most elegant solution for FPGA acceleration
Accelerate Dominant Algorithms

**Opteron™**
- C Application on Opteron
- Calls standard subroutines and return
- If accelerated, calls Accelerated Hardware Subroutines
- Data returned to app
- Application continues to completion on Opteron

**RPU™**
- Algorithms in hardware run 10x to 100x faster
- Results returned to application on Opteron
DS 1000 Design System

DS1002 or DS1004 provide a complete design environment

Get started modifying your application subroutines to run on RPUs

DRC’s partners offer introductory bundles.